



929 SCE

Signal Conditioning In The Embedded World

Slide 1



Class Objectives

When you walk out, you will know

- What makes up the embedded signal chain?
- How do you get the information you need from your signal?
- How can this be applied to a control loop?

Slide 2



Class Agenda

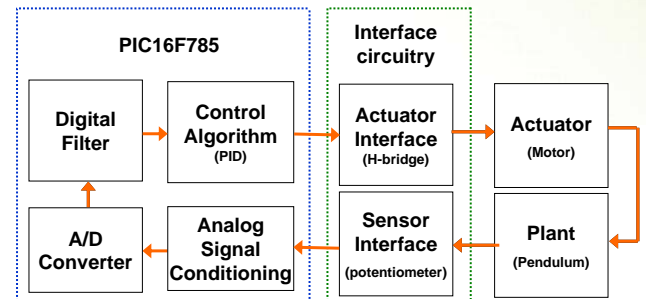
- The Embedded Signal Chain
 - Sensor Interfacing
 - Analog Signal Conditioning
 - Analog to Digital (A/D) Conversion
 - Digital Filtering
 - Control Algorithms
 - Actuator Interfacing

Slide 3



The Embedded Signal Chain

Control Loop for the Inverted Pendulum



Slide 4



The Embedded Signal Chain

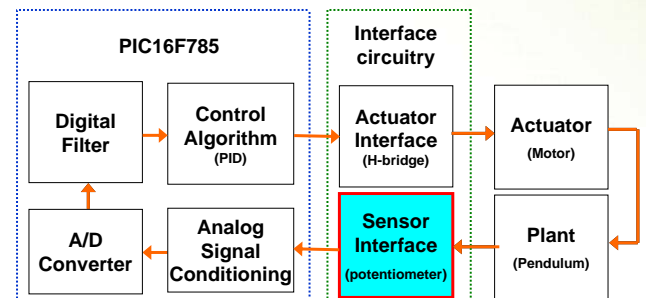


Slide 5



The Embedded Signal Chain

Sensor Interfacing



Slide 6



Sensor Interfacing

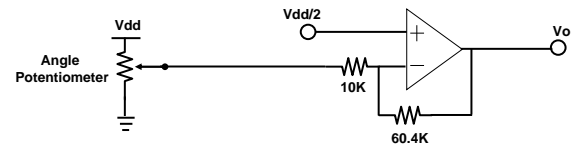
- Sensors generate voltage, resistance, or current
- Must be translated into a variable voltage for analog to digital conversion
- AN990 - Analog Sensor Conditioning Circuits

Slide 7



Sensor Interfacing

- A potentiometer provides mechanical feedback, e.g. angle, position

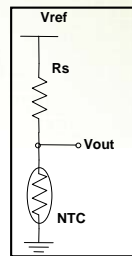


Slide 8



Sensor Interfacing

- Interfacing the thermistor with a voltage divider
 - Voltage divider configuration helps with linearity
 - R_s should be equal to thermistor at mid-point of temperature range
 - For $\pm 25\text{ }^\circ\text{C}$ temperature range $\pm 1\%$ error
- AN685 - Thermistors in Single Supply Temperature Sensing Circuits



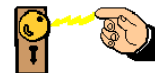
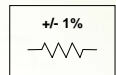
Slide 9



Sensor Interfacing

Possible error sources

- Component tolerances
- Electrical Fast Transient (EFT) signals (e.g. high speed digital, high current/power drive circuits)
- Electrostatic Discharge (ESD)

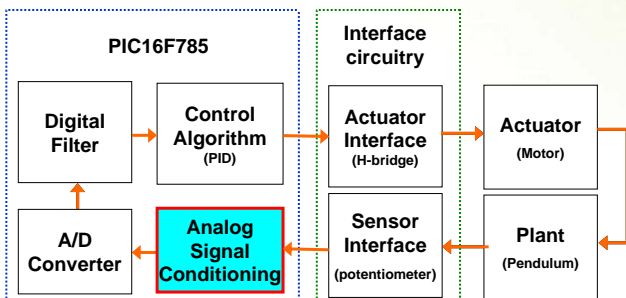


Slide 10



The Embedded Signal Chain

Analog Signal Conditioning

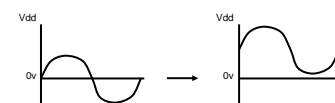
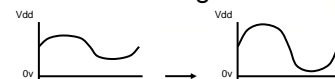


Slide 11



Analog Signal Conditioning

- Why do you need analog signal conditioning?
 - Filter out high frequency noise (e.g. anti-aliasing)
 - Add gain to increase signal resolution
 - Level shifting



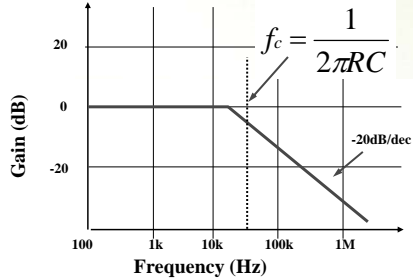
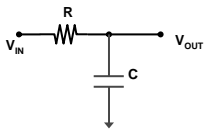
Slide 12



Analog Signal Conditioning

- A single order passive RC filter attenuates by 20 dB/decade beyond f_c or the 3 dB cutoff frequency

$$\frac{V_{out}}{V_{in}} = \frac{1}{1 + sRC}$$

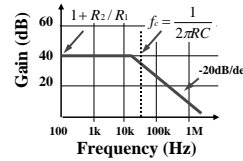


Slide 13

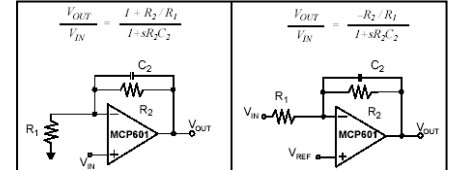


Active Filtering

- Single pole active filters have same frequency response with lower output impedance
- Can gain a signal



Non-inverting or Inverting

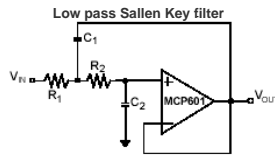


Slide 14



Sallen Key Second Order Filter

- High input impedance, non-inverting, unity gain
- Implement Bessel, Chebyshev, Butterworth, Elliptical or other filter types
- Implement lowpass, highpass, bandpass, band stop

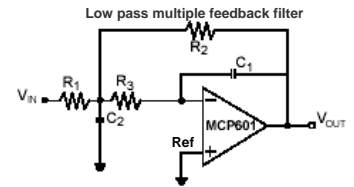


Slide 15



Multiple Feedback Active Filter

- Lower input impedance than Sallen Key
- Inverting with gain or attenuation of $G = -R_2/R_1$
- Less sensitive than Sallen Key to component tolerances
- Pay attention to reference voltage with single supply op-amps

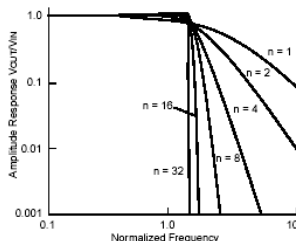


Slide 16



Filter Response

- The higher the order the steeper the response
- Use multiple second order stages to get required results

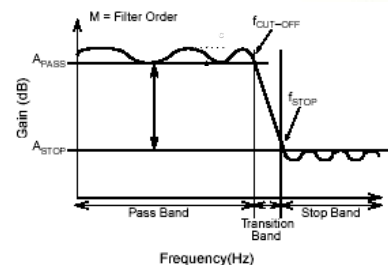


Slide 17



Active Filter Design

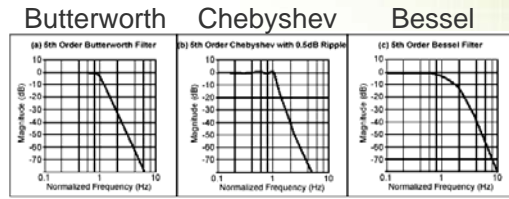
- Use a design program such as Filterlab from Microchip
- Specify key filter performance factors
- The op-amp's open loop bandwidth should be at least 100 times the filters bandwidth at the cutoff frequency



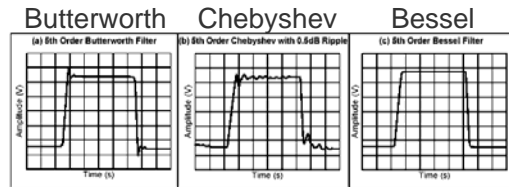
Slide 18



Frequency Response



Step Response

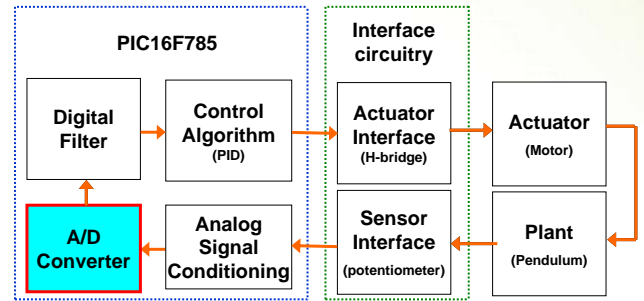


Slide 19



The Embedded Signal Chain

Analog to Digital Conversion

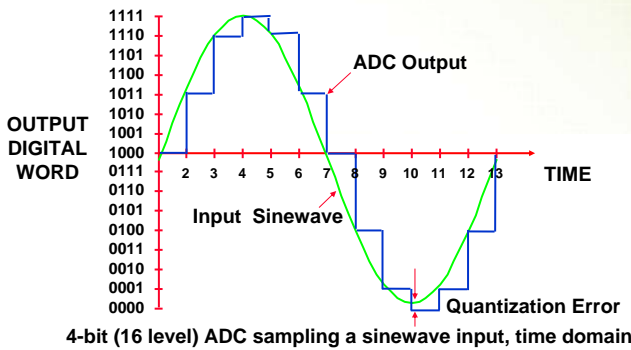


Slide 20



A/D conversion

- Sampling and Quantization



Slide 21



A/D conversion

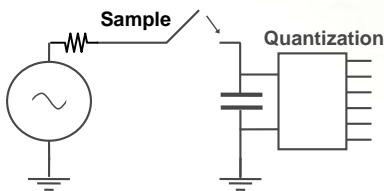
- Ideal signal to noise ratio (SNR) of A/D in dB
 - $SNR = 6.02 \cdot N + 1.76$ [dB]
 - N = A/D resolution in bits
 - Ideal SNR is 62 dB for 10 bit A/D

Slide 22



A/D conversion

- Output impedance



Slide 23



The Alias Effect

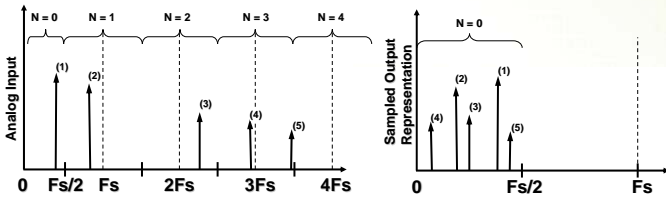
- Satisfy Nyquist Sampling Theorem to retain signal information
 - $F_s \geq 2 \cdot F_{in}$
 - F_s - Sample frequency
 - F_{in} - Input signal frequency
- Get an image F_i if Nyquist conditions are not met
 - $F_i = |n \cdot F_s \pm F_{in}|$
 - n - integer

Slide 24



The Alias Effect

- Input signals above $F_s/2$ will be folded back (aliased) down to a lower frequency

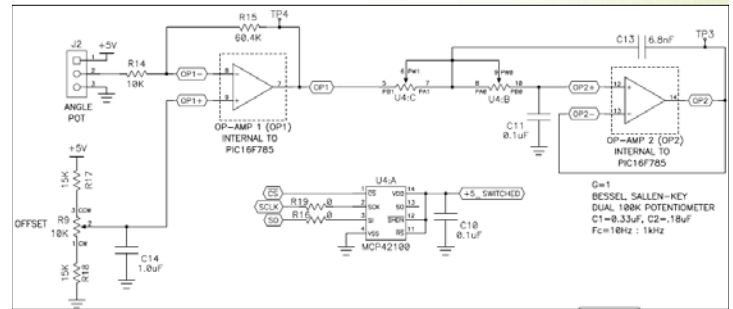


- Use analog filter with $F_c < F_s/2$ before A/D to prevent aliasing

Slide 25



The Alias Effect in action



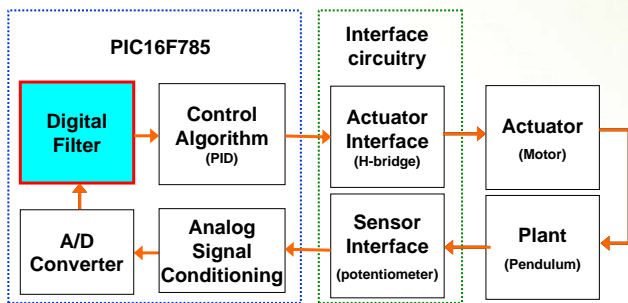
- Filter:
 - Sallen-Key, Bessel, $G=6$, $F_c = 30$ Hz
- What happens without proper anti-aliasing?

Slide 26



The Embedded Signal Chain

Digital Filtering



Slide 27



Digital Filtering

- Finite Impulse Response (FIR) filters do not have feedback
- Infinite Impulse Response (IIR) filters have feedback
- Efficient digital filters



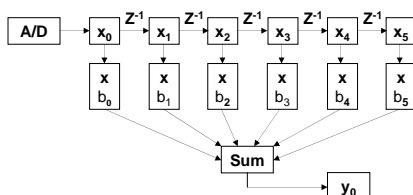
Slide 28



FIR Filter

- Difference equation

$$y(n) = b_0x(n) + b_1x(n-1) + \dots + b_mx(n-m)$$
- With order of "m"

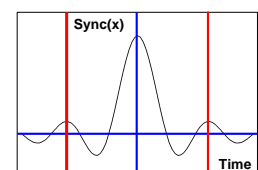
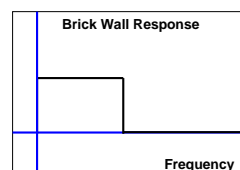


Slide 29



Designing a FIR Filter

- Filter coefficients are based on a given window function $H[n]$
- Different window functions have different performance and filter length requirements



Slide 30



Designing a FIR Filter

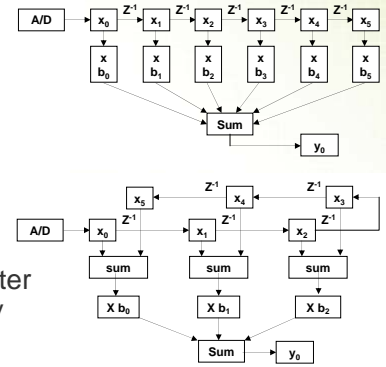
- FIR filter has a constant group delay
- Processor intensive with respect to filter length (order)
- Use a filter coefficient program such as dsPICworks or dsPIC FD Lite from Microchip

Slide 31



Folded FIR Filter

- If coefficients symmetrical ($b_0=b_5$ $b_1=b_4$ $b_2=b_3$)



- Use folded FIR filter to reduce multiply instructions

Slide 32



Moving Average Filters

- Simply taking the moving average of a number of samples, the filter has M points

$$y(n) = \frac{1}{M} \sum_{j=0}^{M-1} x(n-j)$$

- Very good at removing switching noise and random noise
- Is very fast especially if M is a power of 2, i.e. 2, 4, 8, 16 etc → allows one to shift instead of divide

Slide 33



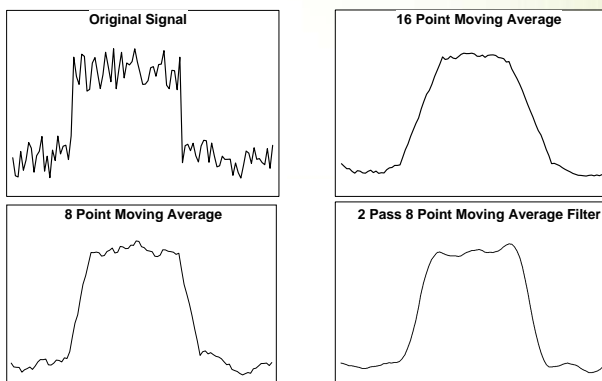
Recursive Implementation

- Two adjacent outputs of a moving average filter
 - $y(50)=x(50)+x(49)+x(48)+x(47)+x(46)+x(45)$
 - $y(51)=x(51)+x(50)+x(49)+x(48)+x(47)+x(46)$
 - $\Rightarrow y(51)=y(50)+x(51)-x(45)$
 - General case: $y(n)=y(n-1)+x(n)-x(n-M)$

Slide 34



Moving Average Filter Examples

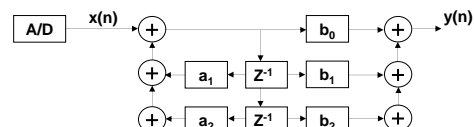


Slide 35



IIR Filter

- A second order IIR filter uses feedback and its difference equation is $y(n) = b_0x(n) + b_1x(n-1) + b_2x(n-2) + a_1y(n-1) + a_2y(n-2)$
- The coefficient b_0 gives the DC gain, typically unity
- Use multiple stages to get desired order



Slide 36



IIR Filter Characteristics

- Normally more efficient than FIR
- Can implement Chebyshev, Butterworth, and Elliptic

Slide 37



IIR Filter Design

- Use a filter coefficient program such as dsPICworks or dsPIC FD Lite from Microchip
- High Q stages can lead to instability due to rounding

Slide 38



Efficient Filters

- Keep bits in the multiplier functions to a minimum and implement own multiplier
- Look into different numbering schemes:
 - Fixed Point signed/unsigned
 - Floating point
 - Two's complement
 - Fractional two's complement or Qx
- Double check filter function for saturation and stability - IIR especially

Slide 39



Efficient Filters

- Can speed up FIR and IIR filters by rounding off the filter coefficients
 - Round by inspection to allow shift and add instead of multiply
- Use two or three shift and add routines instead of a multiply routine
 - Need to multiply $x(n)$ by 10010101 (dec 149)
 - 10010000 (dec 144) \Rightarrow error = -5 ✓
 - 10100000 (dec 160) \Rightarrow error = +11 x
 - $x(n) * 10010101 \approx x(n) \ll 7 + x(n) \ll 4 = x(n) * 2^7 + x(n) * 2^4$
- To multiply $y(n)$ by 00001011 (dec 11)
 - 00001100 (dec 12) \Rightarrow error = +1
 - 00001010 (dec 10) \Rightarrow error = -1
 - In general round down for $y(n)$

Slide 40



Efficient Filters

- Consider convergent (unbiased) rounding instead of conventional or biased rounding - IIR especially
- Use pointers instead of shifting data
- Will a less expensive microcontroller do the job?

Slide 41



Efficient Filters

- Using unsigned filter coefficients is sometimes beneficial
- For scaled filter coefficients between -128 and 127 convert these to 0 to 255
 - Start with difference equation:

$$y(n) = b_0x(n) + b_1x(n-1) + b_2x(n-2) + a_1y(n-1) + a_2y(n-2)$$

$$y(n)^1 = (b_0 + 128)x(n) + (b_1 + 128)x(n-1) + (b_2 + 128)x(n-2) + (a_1 + 128)y(n-1) + (a_2 + 128)y(n-2)$$

$$X(n) = x(n) \cdot 128 + x(n-1) \cdot 128 + x(n-2) \cdot 128$$

$$y(n) = y^1(n) - X(n)$$

Slide 42



Which to Use, Analog or Digital?

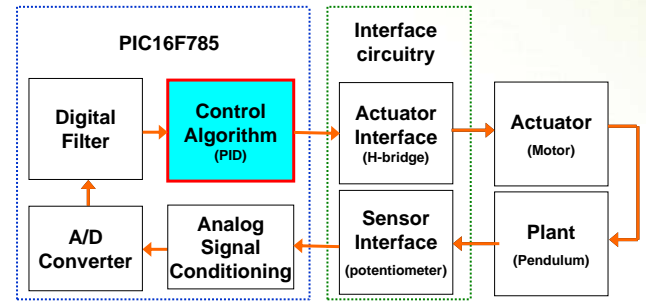
- Digital Filters
 - Highly repeatable and stable
 - Adaptable
 - Always needs power
 - Quantization and rounding noise and errors
- Analog Filters
 - Component variations affects performance
 - Fixed characteristics
 - Can be passive
 - Noise susceptible from conducted, radiated and thermal

Slide 43



The Embedded Signal Chain

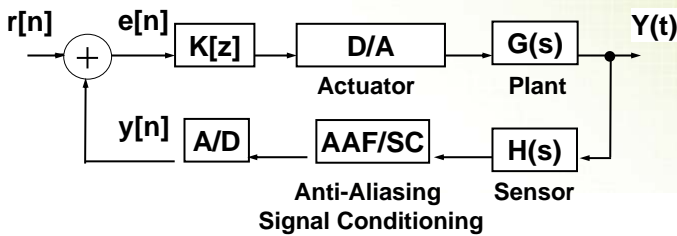
Control Algorithm



Slide 44



Microchip in Control



- r - reference signal
- e[n] - error signal
- K[z] - controller transfer function (PID)
- G(s) - plant transfer function
- H(s) - sensor transfer function
- y(t) - output of controlled system

Slide 45



Microchip in Control

- Use a Proportional-Integral-Derivative (PID) loop to control a system
 - Reduce overshoot
 - Improve stability
 - Improved Performance

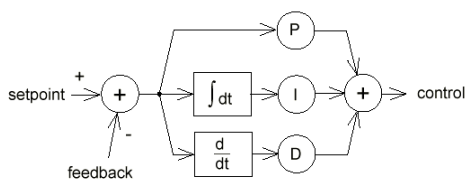
Slide 46



PID Loop

- The PID controller has a transfer function
 - K_p - proportional gain, K_i - integral gain, K_d - derivative gain, $e[z]$ - error input signal to PID loop and $u[z]$ - PID control signal

$$K[z] = \frac{u[z]}{e[z]} = K_p + \frac{K_i}{1-z^{-1}} + K_d(1-z^{-1})$$



Slide 47



PID Loop

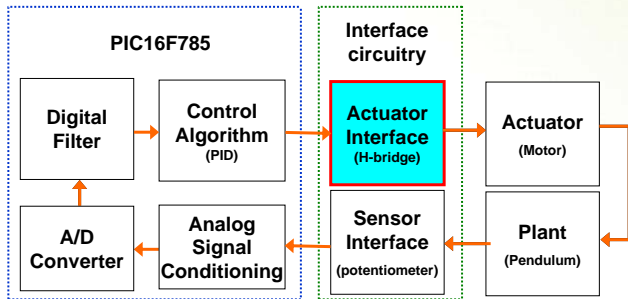
- Tuning the PID parameters
 - Demo
- How does each PID constant affect the System's stability? Overshoot? Response time?

Slide 48



The Embedded Signal Chain

Actuator Interfacing

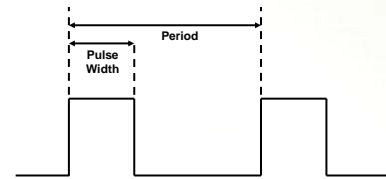


Slide 49



Actuator Interfacing

- Use of PWM for motor control



$$Duty_Cycle = \frac{Pulse_Width}{Period}$$

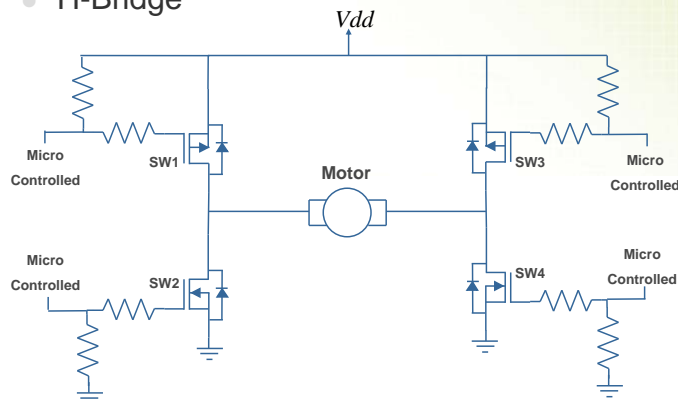
$$Motor_Voltage = Vdd * Duty_Cycle$$

Slide 50



Actuator Interfacing

- H-Bridge



Slide 51



Summary

- The Embedded Signal Chain
 - Sensor Interfacing
 - Analog Signal Conditioning
 - A/D Conversion
 - Digital Filtering
 - PID Control
 - Actuator Interfacing
- Identify possible performance improvement techniques to get the most out of your signal
- PID controllers can be very powerful tools towards improving embedded control

Slide 52



References

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Slide 53



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Slide 54